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|  | **DEPARTMENT OF COMPUTER ENGINEERING** |

**Experiment No. 03**

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| Semester | S.E-Semester III – Computer Engineering |
| Subject | Digital Logic and Computer Architecture |
| Subject Professor In-charge | Prof. Avinash Shrivas |
| Assisting Teachers | Prof. Avinash Shrivas |

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| Student Name – Deep Salunkhe |
| Roll Number – 21102A0014 |
| Division and Batch – Division A, Batch 1 |
| Date of Implementation – 17/07/2022 |
| Experiment Title: Universal Gates |
| **Theory:**  NAND and NOR gates are also known as universal gates as all the gates can be recreated using these gate |
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| **Implementation** |
| **Conclusion:** |